

AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111

Serial Number: 10/628,772

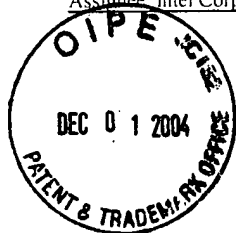
Filing Date: July 28, 2003

Title: MEMORY ACCESS CONTROL SYSTEM, APPARATUS, AND METHOD

Assignee: Intel Corporation

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Dkt: 884.480US2 (INTEL)



IN THE SPECIFICATION

Please amend the specification as follows:

The paragraph beginning at page 1, line 6 is amended as follows:

This application is a continuation of U.S. Patent Application Serial No. 09/902,998, filed July 11, 2001, now issued as U.S. Patent No. 6,615,329, which is incorporated herein by reference.